

## 1. Cadence Sigrity SI Training Course (2 Days Course)

- Cadence Sigrity SI Solution Overview
- Board file translation & Option parameters
- Prepare Analysis Process (Net Setting, Stackup check, etc.)
- SPEED2000 Workflow for Analysis
- TDR/TDT & General SI Simulation
- Trace Impedance/Coupling & DDR Simulation
- EMI/ESD Simulation
- System Explorer Topology Design & Simulation
- SystemSI Simulation Channel Design
- SystemSI Simulation Setup & Analysis
- IBIS-AMI Model Analysis & Generation

The training content and sequence may vary slightly for course efficiency.